Saleh
# JAMALI GOLZAR
*Curriculum Vitae*

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Saleh Jamali

## Education

2023 **Ph.D Student, Computer Science**, *University of Salerno*, Fisciano, Italy
- **Supervisor:** Prof. Biagio Cosenza
- **Topic:** High-Performance Computing: Optimization on Intel HW and SW Platforms.

2016-2019 **M.Sc Electrical Engineering, Integrated Circuit Design**, *University of Tabriz*, Tabriz, Iran, GPA-17.24/20
- **Supervisor:** Prof. Ghader Karimian
- **Adviser:** Prof. Maryam Shoaran
- **Dissertation Topic:** 3D Deep Learning on Heterogeneous Systems, Hardware Implementation
- GPU and FPGA-based acceleration of Dynamic Graph CNN Model for learning on point-clouds

2012-2016 **B.Sc Electrical Engineering**, *University of Tabriz*, Tabriz, Iran, GPA-16.40/20

## Publications

### Journals

2026 **"Optimizing the LiGen Drug Discovery Pipeline for Intel Max GPUs"**
- **Authors**: S. Jamali Golzar (First Author), L. Carpentieri, A. De Caro, B. Cosenza, D. Gadioli, G. Accordi, G. Palermo, F. Ficarelli, D. Gregori, and A. R. Beccari
- **Status**: Accepted to **PDP'26**, *34th Euromicro International Conference on Parallel, Distributed, and Network-Based Processing*

2025 **"Demystifying Power-of-Two Quantization: Benchmarking Inference on AVX and RVV"**
- **Authors**: S. Jamali (First Author), G. Pagano, B. Cosenza
- **Status**: Accepted to **ITADATA'25-Workshops**, *Scientific HPC in the pre-Exascale era (2nd edition)*

2022 **"DGCNN on FPGA: Acceleration of the Point Cloud Classifier Using FPGAs"**
- **DOI**: https://doi.org/10.1007/s00034-022-02179-0
- **Journal**: Circuits, Systems, and Signal Processing (Springer)
- **Authors**: S. Jamali (First Author), M. Shoaran, G. Karimian, M. Fattahi
- **Status**: Published (February 2023)

## Experience

2026 **Visiting Researcher**, *TU Wien*, Vienna, Austria
- **Supervisor:** Prof. Sascha Hunold (Parallel Computing Lab)
- Apr.–Sep. 2026 (6 months)

2025–2026 **Intern**, *E4 Computer Engineering SpA*, Italy
- Sep. 2025–Mar. 2026 (6 months)

2017-2019 **Embedded and IoT Systems Design**, *Ava Mechatronics*, Tabriz, Iran
- Part-time employment from Oct. 2017 to Aug. 2018

2016 **Extern**, *Ava Mechatronics*, Tabriz, Iran

## Skills

Hardware FPGA Design (Verilog, HLS C++), PCB Design (Altium Designer), Embedded Systems (NXP, STM, Atmel, Cypress, Nordic Semi)

Software GPGPU (CUDA, SYCL, OpenCL), ML/DL and Model Compression, Image Processing

Tools Git, Bash, Linux

## Selected Courses

### M.Sc and B.Sc Courses

2016 **VHDL**, *University of Tabriz*, Tabriz, Iran, GPA-18/20

2016 **VLSI**, *University of Tabriz*, Tabriz, Iran, GPA-19/20

2015 **Microprocessors**, *University of Tabriz*, Tabriz, Iran, GPA-20/20

2015 **Computer Architecture**, *University of Tabriz*, Tabriz, Iran, GPA-19.5/20

2012 **Logic Circuits**, *University of Tabriz*, Tabriz, Iran, GPA-20/20

### Online Courses

2016 **Machine Learning**, *Stanford University*, Coursera

### Certificates of Attendance

2025 **Multi-GPU Programming Bootcamp**, *(Online)*, EuroCC2

## Languages

Azeri (Native), Persian (Native), English (TOEFL iBT: 102, UNISA C1)